

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,180,477 B2  
APPLICATION NO. : 10/706451  
DATED : February 20, 2007  
INVENTOR(S) : David Howell

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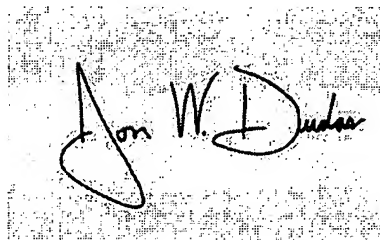
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 7, line 5, delete "transistor-transistor logic".

Column 12, line 1, change "deice" to --device--.

Signed and Sealed this

Seventeenth Day of April, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a rectangular area with a light gray, textured background.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*